

**CprE 588 – Embedded Computer Systems**  
**Weekly Reading Review #7**  
**Assigned: Week 7**  
**Reviewed: Week 8**

**Directions:**

- These weekly readings are intended to support in-class discussion.
- An in-class review activity will structure the discussion, as well as provide the instructor and student with some regular feedback on learning in the course.
- These activities will be recorded (based on completion, not correctness) as a component of the student's grade in the course.
- Submission is via the "Assessments" tab in WebCT.

**Reading:**

- [16] M. Olivarez and B. Beasley, "Smart Speed Technology - Results of Modeling for Embedded Applications", *Technical White Paper*, Freescale Semiconductor, July 2007. ([link](#))

**Review:**

What effect does the placement of the L2 cache in the simulated architectural model have on the key design metrics (performance and energy)? How could architectural issues such as this one be modeled using the SpecC methodology?